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MANCHESTER, N.H., July 29, 2021 (GLOBE NEWSWIRE) -- Allegro MicroSystems, Inc. ("Allegro" or the "Company") (Nasdaq:ALGM), a global leader in power and sensing semiconductor solutions for motion ...

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Learn the basic properties and designs of modern VLSI devices, as well as the factors affecting performance, with this thoroughly updated second edition. The first edition has been widely adopted as a standard textbook in microelectronics in many major US universities and worldwide. The internationally renowned authors highlight the intricate interdependencies and subtle trade-offs between various practically important device parameters, and provide an in-depth discussion of device scaling and scaling limits of CMOS and bipolar devices. Equations and parameters provided are checked continuously against the reality of silicon data, making the book equally useful in practical transistor design and in the classroom. Every chapter has been updated to include the latest developments, such as MOSFET scale length theory, high-field transport model and SiGe-base bipolar devices.

The new edition of the most detailed and comprehensive single-volume reference on major semiconductor devices The Fourth Edition of Physics of Semiconductor Devices remains the standard reference work on the fundamental physics and operational characteristics of all major bipolar, unipolar, special microwave, and optoelectronic devices. This fully updated and expanded edition includes approximately 1,000 references to original research papers and review articles, more than 650 high-quality technical illustrations, and over two dozen tables of material parameters. Divided into five parts, the text first provides a summary of semiconductor properties, covering energy band, carrier concentration, and transport properties. The second part surveys the basic

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building blocks of semiconductor devices, including p-n junctions, metal-semiconductor contacts, and metal-insulator-semiconductor (MIS) capacitors. Part III examines bipolar transistors, MOSFETs (MOS field-effect transistors), and other field-effect transistors such as JFETs (junction field-effect-transistors) and MESFETs (metal-semiconductor field-effect transistors). Part IV focuses on negative-resistance and power devices. The book concludes with coverage of photonic devices and sensors, including light-emitting diodes (LEDs), solar cells, and various photodetectors and semiconductor sensors. This classic volume, the standard textbook and reference in the field of semiconductor devices: Provides the practical foundation necessary for understanding the devices currently in use and evaluating the performance and limitations of future devices Offers completely updated and revised information that reflects advances in device concepts, performance, and application Features discussions of topics of contemporary interest, such as applications of photonic devices that convert optical energy to electric energy Includes numerous problem sets, real-world examples, tables, figures, and illustrations; several useful appendices; and a detailed solutions manual Explores new work on leading-edge technologies such as MODFETs, resonant-tunneling diodes, quantum-cascade lasers, single-electron transistors, real-space-transfer devices, and MOS-controlled thyristors Physics of Semiconductor Devices, Fourth Edition is an indispensable resource for design engineers, research scientists, industrial and electronics engineering managers, and graduate students in the field.

During the first decade following the invention of the transistor, progress in semiconductor device technology advanced rapidly due to an effective synergy of technological discoveries and physical understanding. Through physical

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reasoning, a feeling for the right assumption and the correct interpretation of experimental findings, a small group of pioneers conceived the major analytic design equations, which are currently to be found in numerous textbooks. Naturally with the growth of specific applications, the description of some characteristic properties became more complicated. For instance, in integrated circuits this was due in part to the use of a wider bias range, the addition of inherent parasitic elements and the occurrence of multi dimensional effects in smaller devices. Since powerful computing aids became available at the same time, complicated situations in complex configurations could be analyzed by useful numerical techniques. Despite the resulting progress in device optimization, the above approach fails to provide a required compact set of device design and process control rules and a compact circuit model for the analysis of large-scale electronic designs. This book therefore takes up the original thread to some extent. Taking into account new physical effects and introducing useful but correct simplifying assumptions, the previous concepts of analytic device models have been extended to describe the characteristics of modern integrated circuit devices. This has been made possible by making extensive use of exact numerical results to gain insight into complicated situations of transistor operation.

This book describes the bottleneck faced soon by designers of traditional CMOS devices, due to device scaling, power and energy consumption, and variability limitations. This book aims at bridging the gap between device technology and architecture/system design. Readers will learn about challenges and opportunities presented by “beyond-CMOS devices” and gain insight into how these might be leveraged to build energy-efficient electronic systems.

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Proceedings of the International Conference held at Seville, Spain, October 27-31, 1986.

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