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IPC Users Guide. Inter-
Processor Communication
(IPC) provides a processor-
agnostic API which can be

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used for communication between processors in a multi-processor environment (inter-core), communication to other threads on same processor (inter-process), and communication to peripherals (inter-device).

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The API supports message passing, streams, and linked lists.

~~IPC Users Guide - Texas
Instruments Wiki~~

IPC is a component
containing packages that are

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designed to allow communication between processors in a multi-processor environment and communication to peripherals. This communication includes message passing, streams,

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and linked lists. These work transparently in both uni-processor and multi-processor configurations.

~~SYS/BIOS Inter-Processor
Communication (IPC) 1.25
(Rev. E)~~

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The IPC product contains an examples/archive directory with device-specific examples. Once identifying your device, the examples can be unzipped anywhere on your build host. Typically once unzipped, the user

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edits the example's
individual products.mak file
and simply invokes make.

~~IPC Users Guide/Examples~~
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IPC can be used to
communicate with the

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following: other threads on
the same processor. threads
on other processors running
SYS/BIOS. threads on GPP
processors running a High-
Level Operating System
(HLOS) IPC was designed with
the needs of a wide variety

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of users in mind.

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Cdd Ipc User Guide .

Introduction. This document

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```
details Cdd Ipc module
implementations. Supported
AUTOSAR Release : 4.3.1;
Supported Configuration
Variants : Pre-Compile; ...
Received ping 9 Iteration 1
from MCU 1 1 CDD_IPC_APP :
Received ti.ipc4.ping-pong
```

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```
as ctrl MSG from MCU 2 0  
CDD_IPC_APP : Received ping  
0 Iteration 10 from MCU 2 0  
CDD_IPC ...
```

~~MCUSW: Cdd Ipc User Guide~~
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The ti.sdo.ipc.Notify module

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manages the multiplexing/demultiplexing of software interrupts over hardware interrupts. In order to use any Notify APIs, you must first call `Ip_start()`. This sets up all the necessary Notify

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drivers, shared memory, and
interprocessor interrupts.

~~IPC Users Guide/Notify
Module - Texas Instruments
Wiki~~

IPC provides an
implementation of the

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IMessageQTransport interface called `ti.sdo.ipc.transports`.
`.TransportShm` (shared memory). You can write other implementations to meet your needs. When a transport is created via a transport-specific `create()` call, a

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remote processor ID (defined via the MultiProc module) is specified.

~~IPC Users Guide/MessageQ
Module - Texas Instruments
Wiki~~

Overview. IPC is a generic

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term of Inter-Processor
Communication referred
widely in the industry, but
also a package in TI
Processor SDK for multi-core
communication. In generic
usage, there are different
ways for multi-core

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communication such as OpenCL, DCE, TI-IPC, and etc. In TI's IPC package, it uses a set of modules to facilitate the inter-processor communication.

~~3.7. IPC — Processor SDK~~

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~~Linux Documentation~~

Download Ebook Ti Ipc User Guide Ti Ipc User Guide This wiki-based user's guide is the new home of the Inter-Processor Communication (IPC) User's Guide (SPRUG06). That PDF-based

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document will no longer be updated. This user's guide contains the topics in the following list. It also links to API

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2019: User guide: User's
Guide for Powering DRA829V
and TDA4VM with the
TPS6594-Q1 PMICs: Jul. 09,

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IPC Overview. The IPC product provides software connectivity between multiple processors. Each processor may run either an HLOS (e.g. Linux, QNX) or an RTOS (e.g. SYS/BIOS). IPC is

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an open source project,
currently managed with git,
and maintained at
<http://git.ti.com/ipc/ipcdev>
. Full source browsing,
including all changes, are
available here .

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The only book to offer special coverage of the fundamentals of multicore DSP for implementation on the TMS320C66xx SoC This

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unique book provides readers with an understanding of the TMS320C66xx SoC as well as its constraints. It offers critical analysis of each element, which not only broadens their knowledge of the subject, but aids them

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in gaining a better understanding of how these elements work so well together. Written by Texas Instruments' First DSP Educator Award winner, Naim Dahnoun, the book teaches readers how to use the

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development tools, take advantage of the maximum performance and functionality of this processor and have an understanding of the rich content which spans from architecture, development

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tools and programming models, such as OpenCL and OpenMP, to debugging tools. It also covers various multicore audio and image applications in detail. Additionally, this one-of-a-kind book is supplemented

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with: A rich set of tested
laboratory exercises and
solutions Audio and Image
processing applications
source code for the Code
Composer Studio (integrated
development environment from
Texas Instruments) Multiple

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tables and illustrations
With no other book on the
market offering any coverage
at all on the subject and
its rich content with twenty
chapters, *Multicore DSP:
From Algorithms to Real-time
Implementation on the*

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TMS320C66x SoC is a rare and much-needed source of information for undergraduates and postgraduates in the field that allows them to make real-time applications work in a relatively short period

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of time. It is also
incredibly beneficial to
hardware and software
engineers involved in
programming real-time
embedded systems.

This book includes a range

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of techniques for developing digital signal processing code; tips and tricks for optimizing DSP software; and various options available for constructing DSP systems from numerous software components.

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This book constitutes the refereed proceedings of the 9th International Workshop on OpenMP, held in Canberra, Australia, in September 2013. The 14 technical full papers presented were

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carefully reviewed and selected from various submissions. The papers are organized in topical sections on proposed extensions to OpenMP, applications, accelerators, scheduling, and tools.

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Includes index.

An introduction to embedding

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systems for C and C++
programmers encompasses such
topics as testing memory
devices, writing and erasing
Flash memory, verifying
nonvolatile memory contents,
and much more. Original.
(Intermediate).

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By offering the new Service Routing Certification Program, Alcatel-Lucent is extending their reach and knowledge to networking

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overviews of the Internet,
IP routing and the IP layer,
and the practice of opening
the shortest path first.

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